

FIG. 1A

FIG. 1B

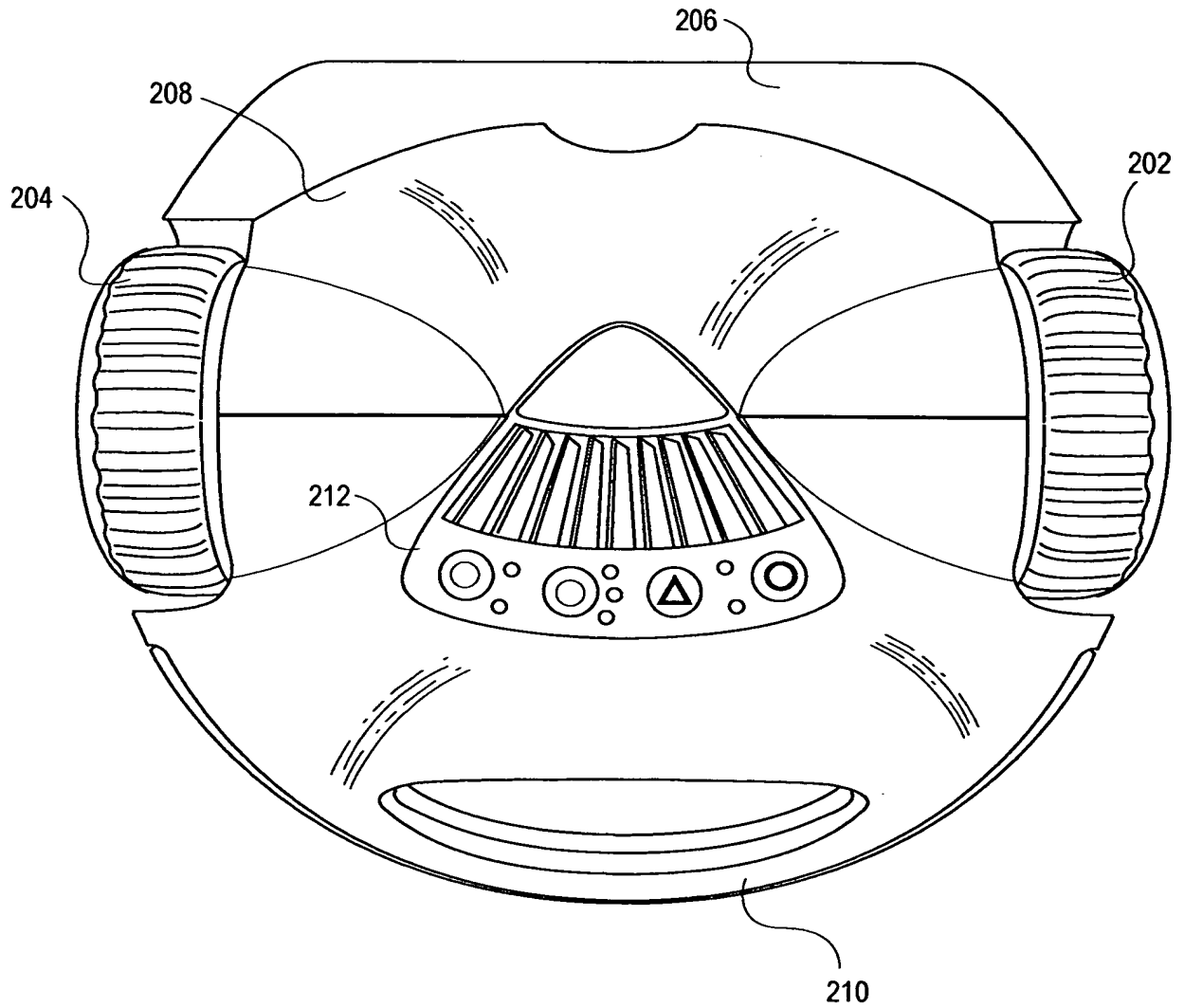


FIG. 2A

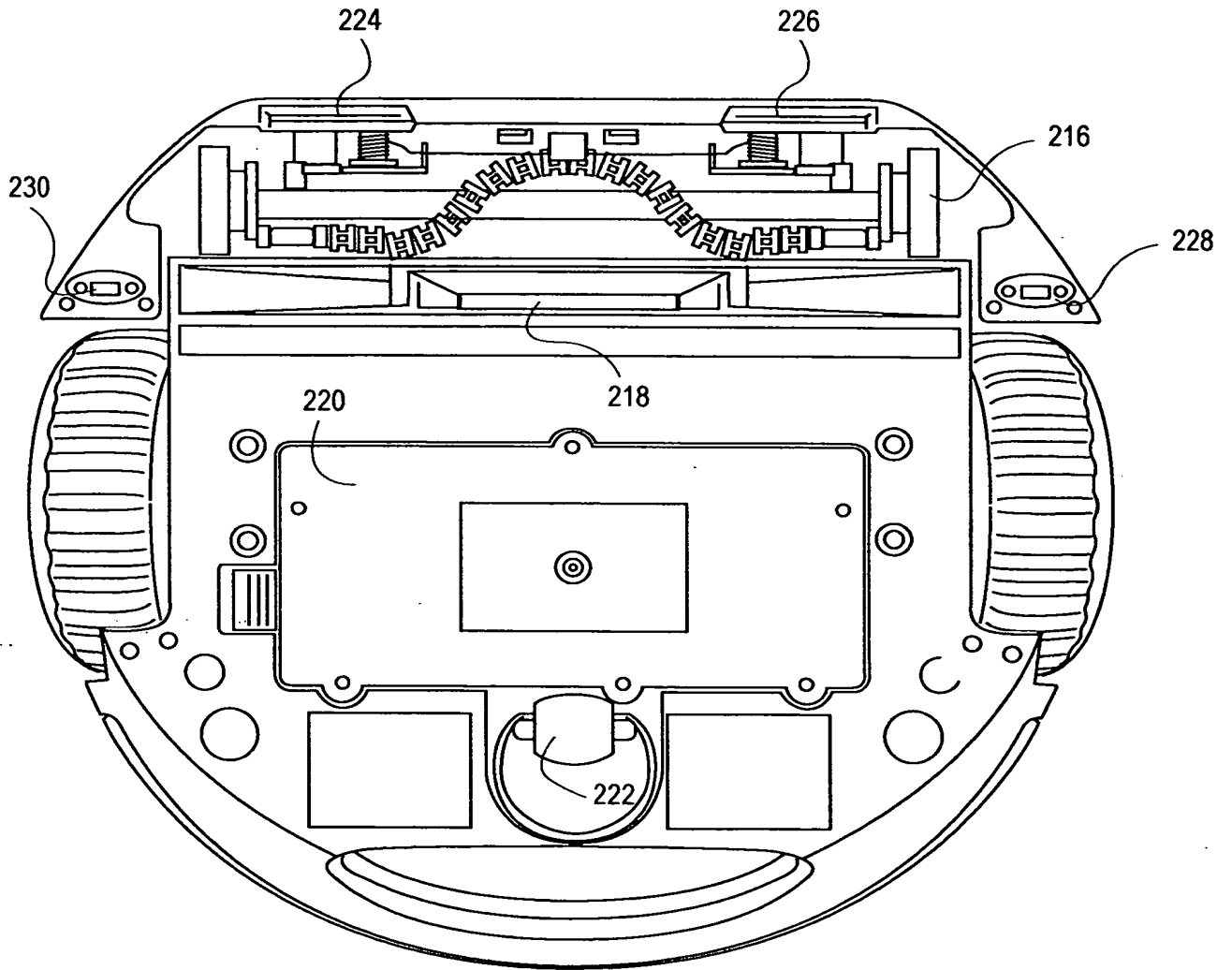


FIG. 2B

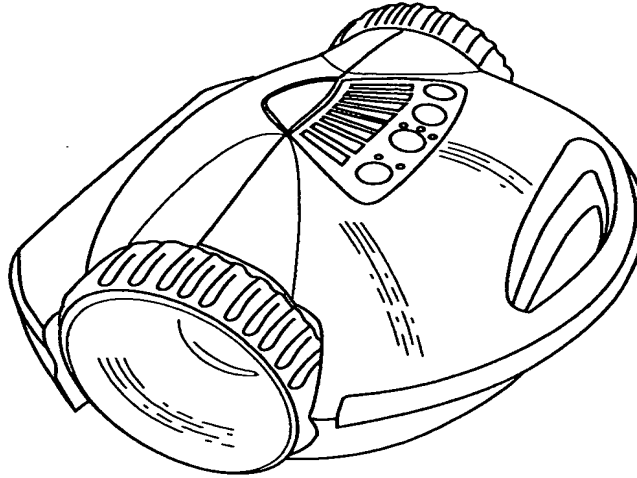


FIG. 2C

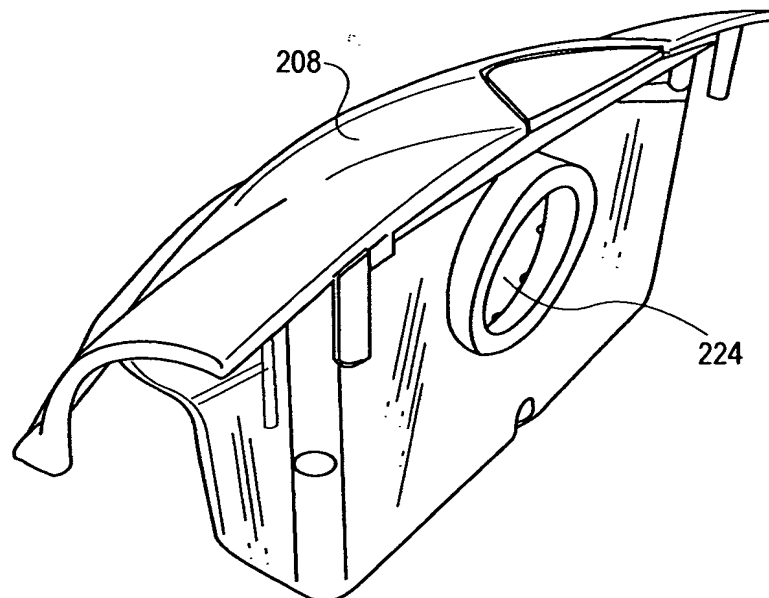


FIG. 2D

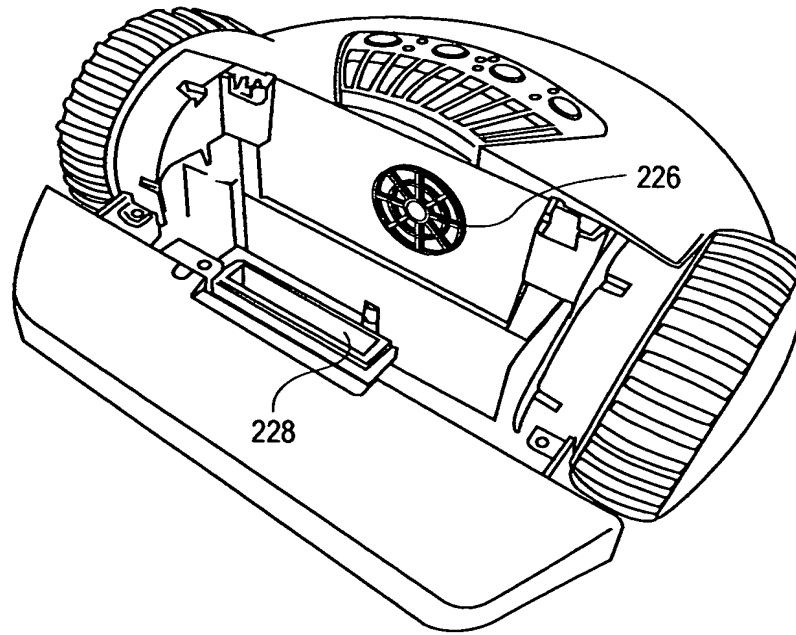


FIG. 2E

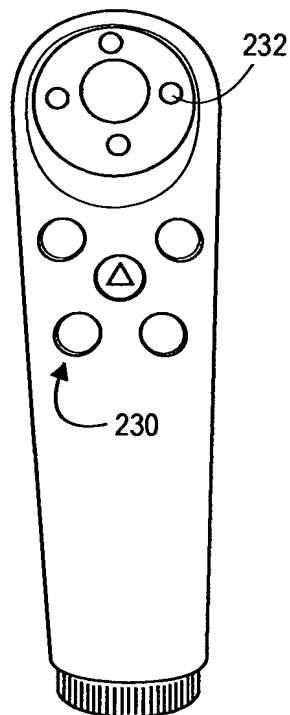


FIG. 2F

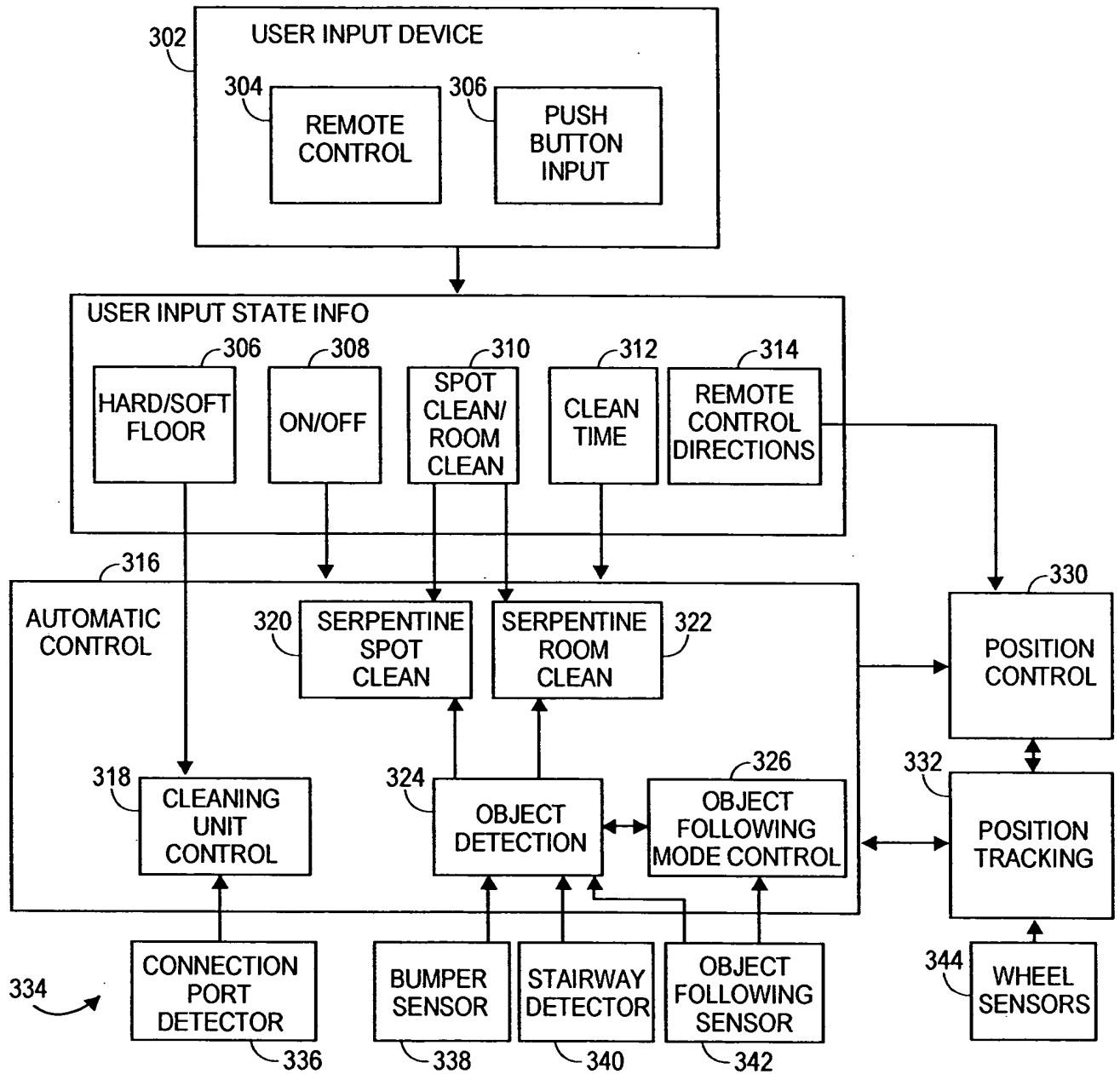


FIG. 3

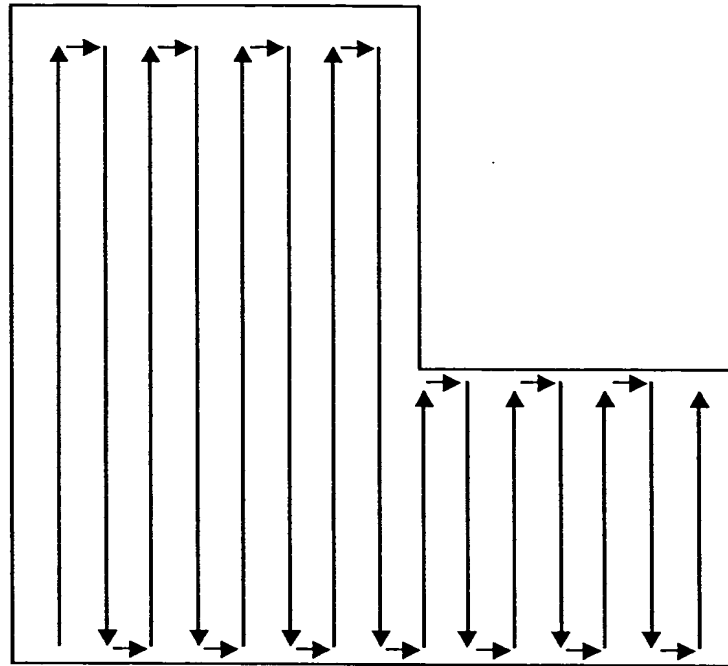


FIG. 4

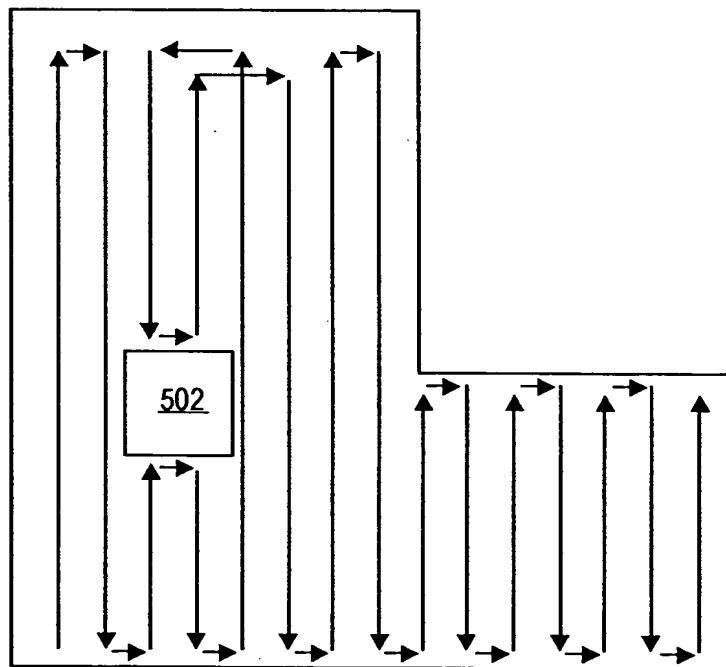


FIG. 5

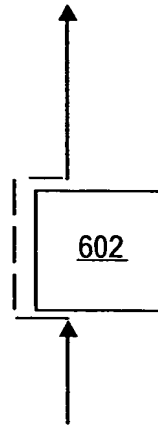


FIG. 6

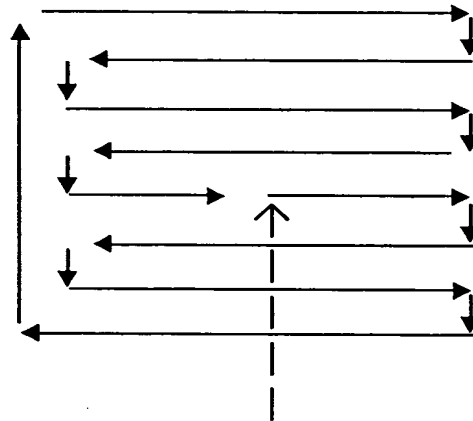


FIG. 7

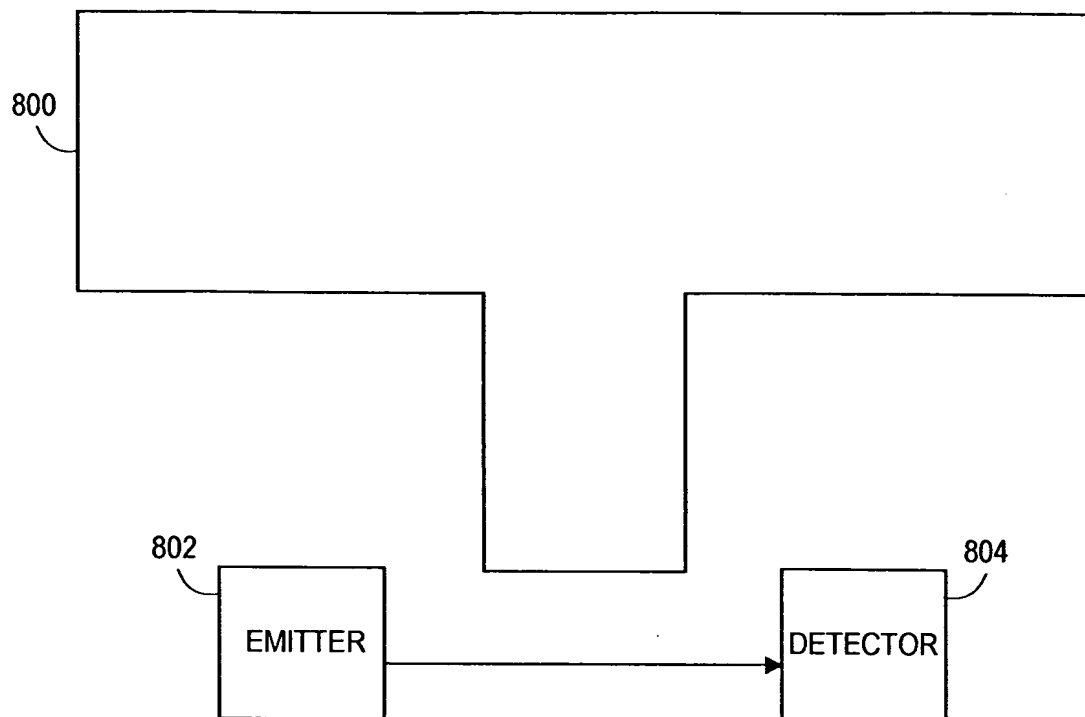


FIG. 8A

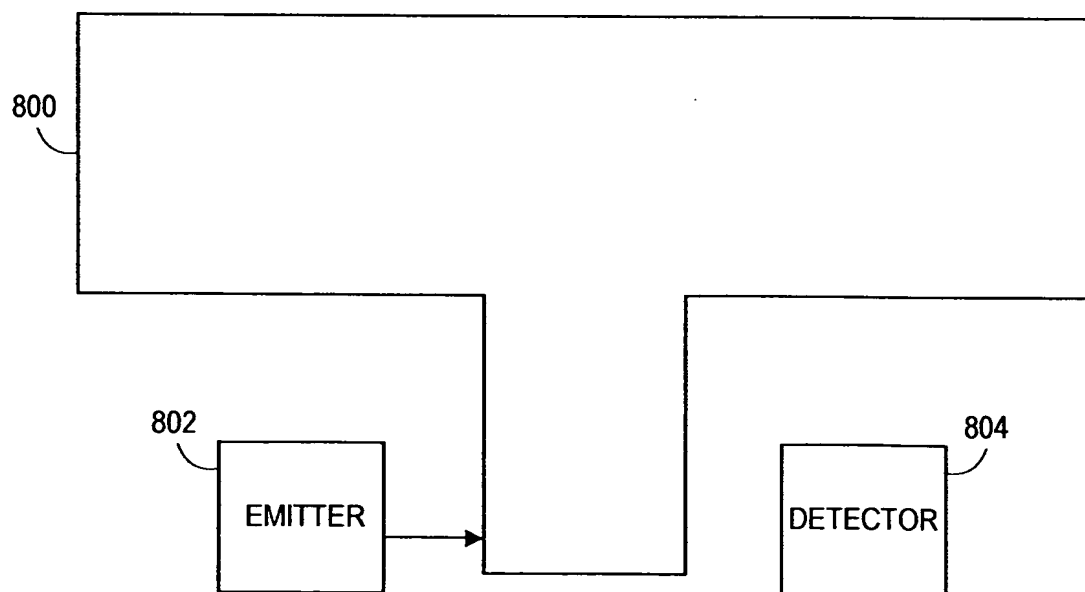


FIG. 8B

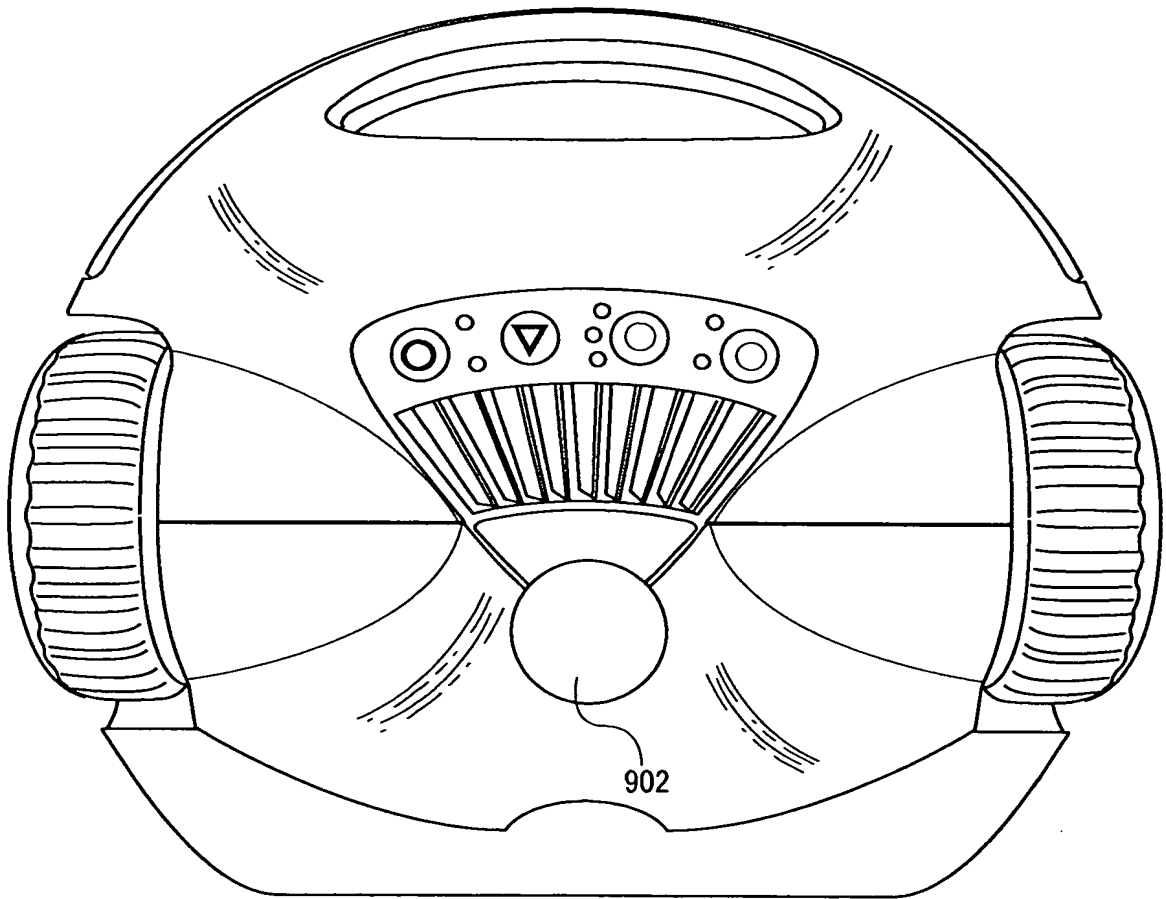


FIG. 9A

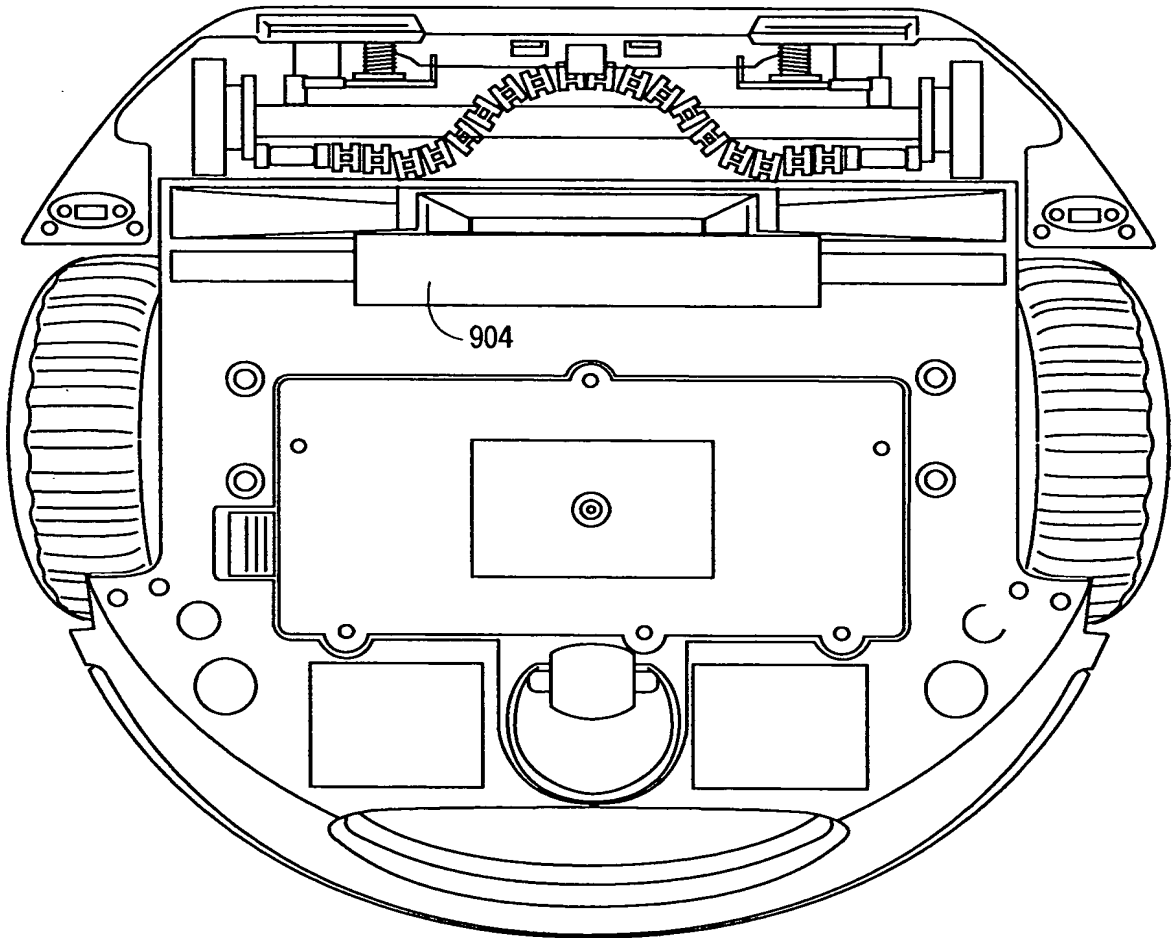
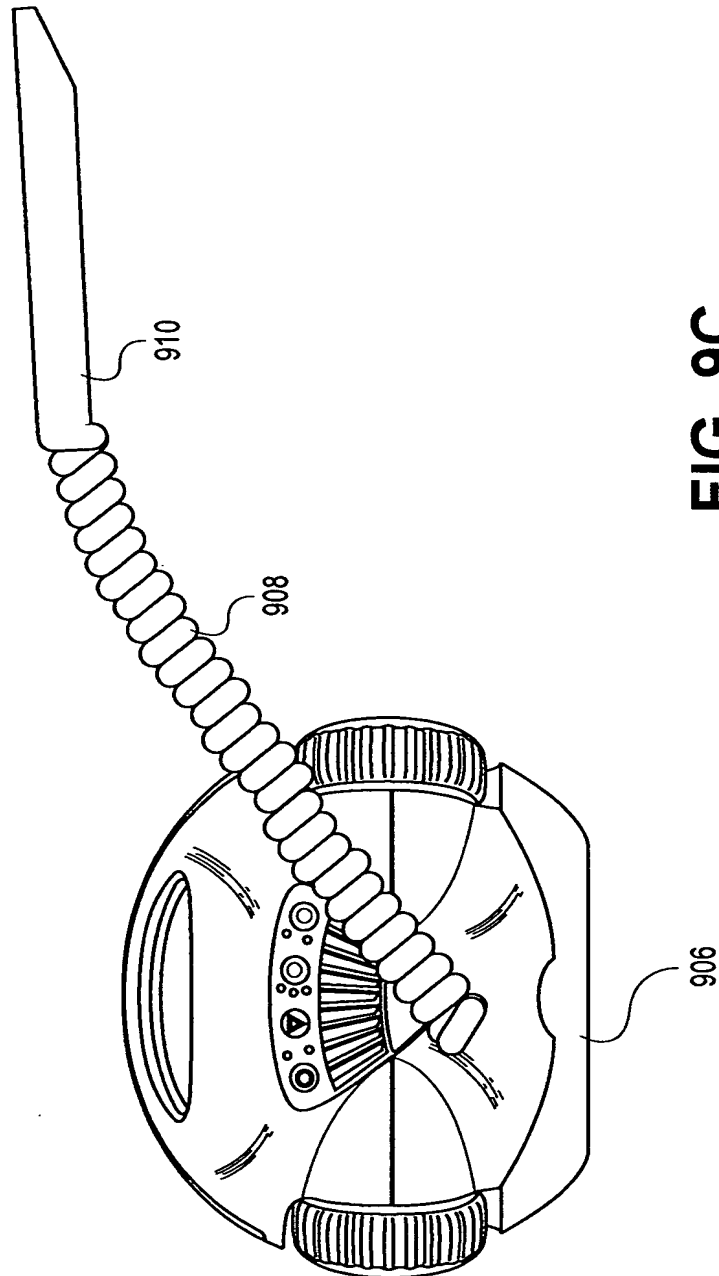


FIG. 9B



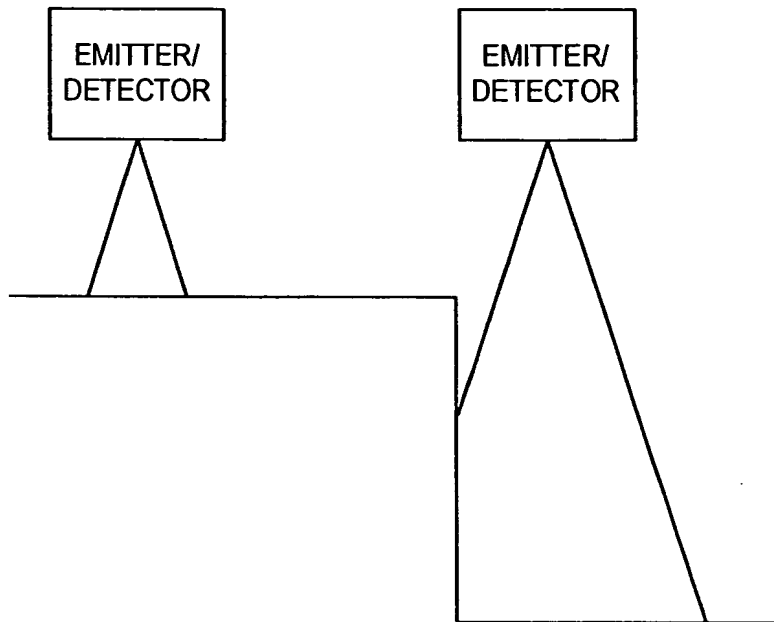


FIG. 10A

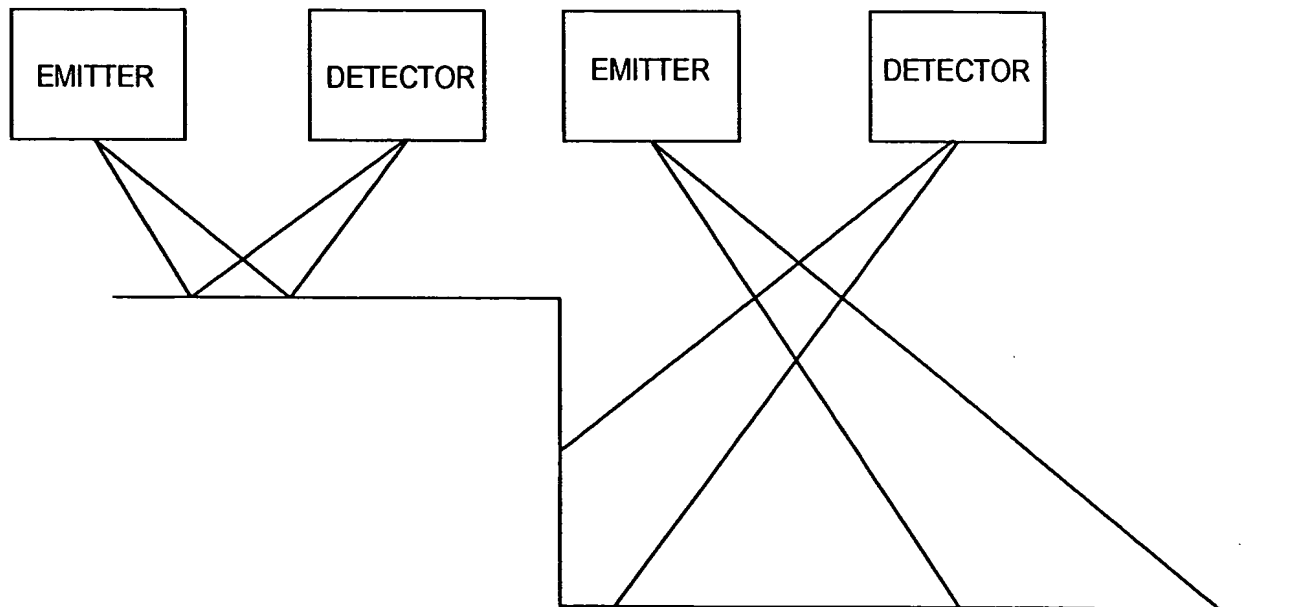


FIG. 10B

FIG. 11A

FIG. 11B

FIG. 11C

FIG. 11D

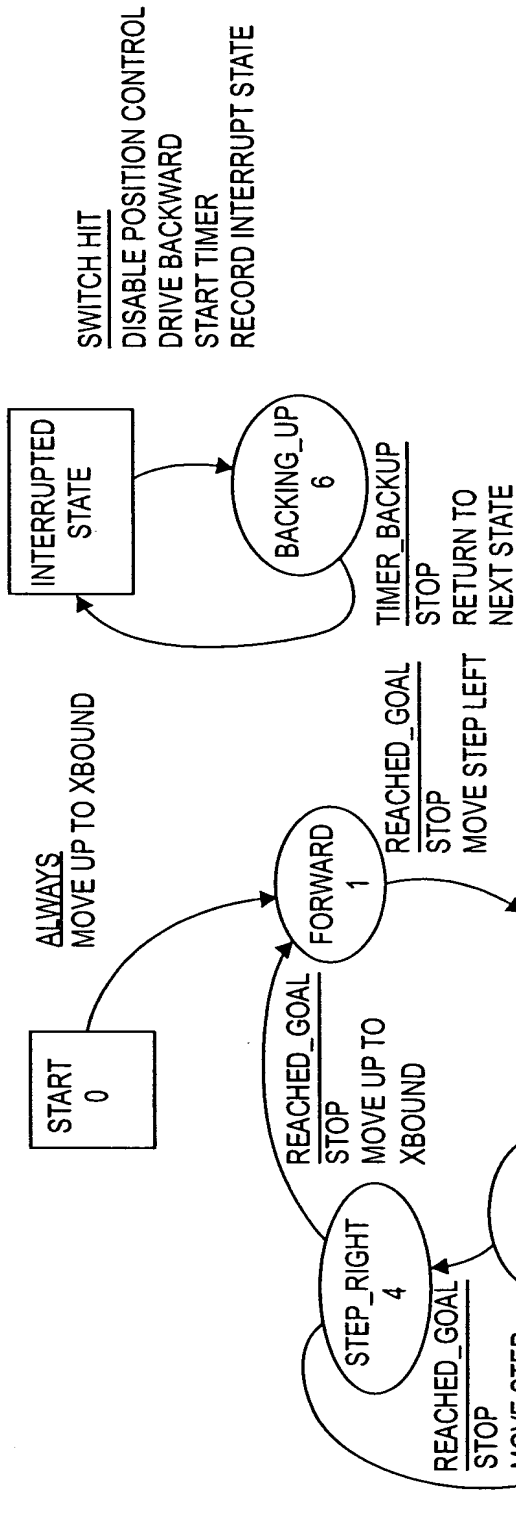


FIG. 12A

FIG. 12B

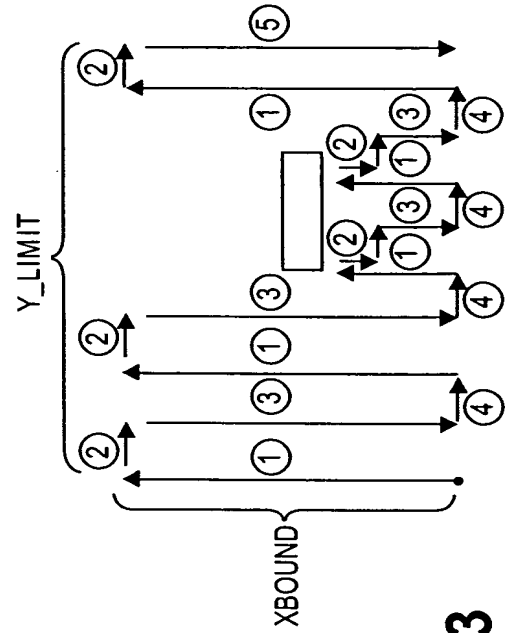
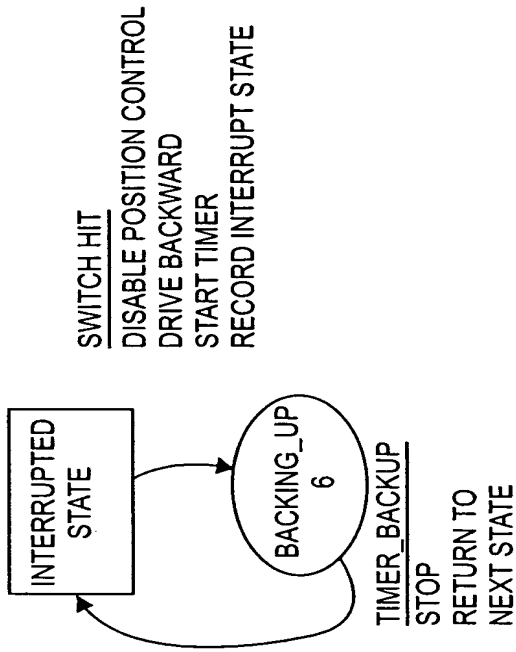


FIG. 13



SWITCH HIT
DISABLE POSITION CONTROL
DRIVE BACKWARD
START TIMER
RECORD INTERRUPT STATE

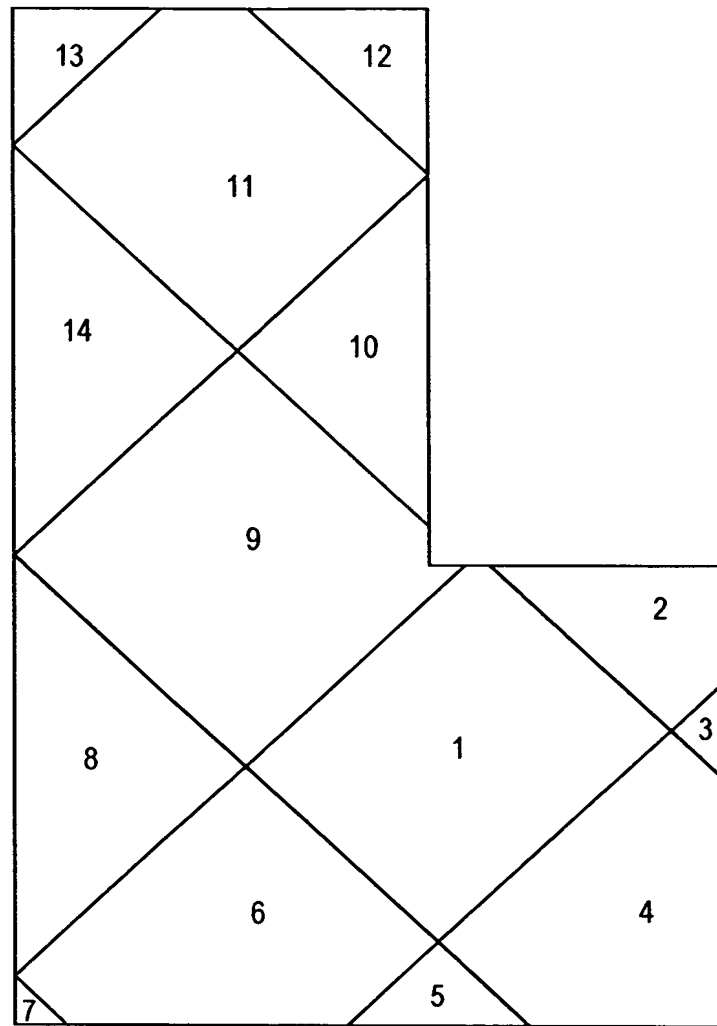


FIG. 14

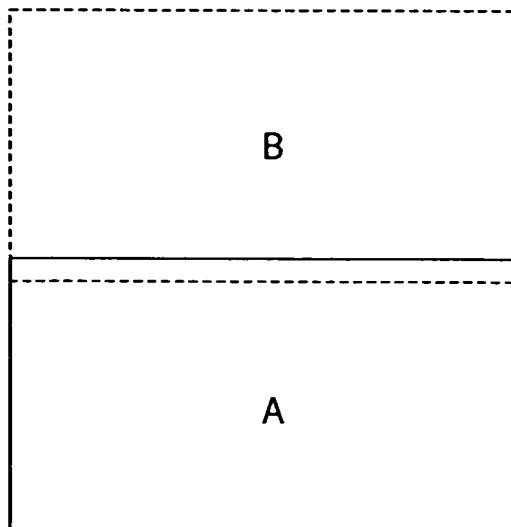


FIG. 15

